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TRANSMITTAL  FORM  (to be used for all correspondence after initial filing)		Application / Conf. No.			10/618,329 / 5287	
		Filing Date			July 11, 2003	
		First Named Inventor		r	Andy T. Nguyen	
		Examiner Name			Unknown	
Mail Stop: AMENDMENT		Art Unit			2816	
Express Mail Receipt No.		Patent N	lo.	-		
Total Number of Pages in This Submission		Attorney Docket Number		nber	X-1464 US	
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X-1464 US 10/618,329 PATENT Conf. No. 5287

IN THE UNITED STATES PATENT OFFICE

Applicant:

Andy T. Nguyen

Assignee:

Xilinx, Inc.

Title:

Counter-Based Clock Doubler Circuits and Methods with Optional

**Duty Cycle Correction and Offset** 

Serial No.:

10/618,329

File Date:

7/11/2003

Examiner:

Cassandra F. Cox

Art Unit:

2816

Docket No.:

X-1464 US

Conf. No.:

5287

Mail Stop AMENDMENT COMMISSIONER FOR PATENTS P.O. Box 1450 Alexandria, VA 22313-1450

## AMENDMENT IN RESPONSE TO FIRST OFFICE ACTION

In response to the Office Action mailed September 8, 2004, Applicant submits the following amendments and remarks.

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Remarks begin on page 9 of this paper.